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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Hong Koo Kim

Application No.: 09/747,779

Filed: December 22, 2000

For: FABRICATION METHOD AND STRUCTURE FOR FERROELECTRIC NONVOLATILE MEMORY FIELD EFFECT TRANSISTOR

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Examiner:

Marcos D. Pizarro Crespo

Art Unit:

2814

RESPONSE TO RESTRICTION

REQUIREMENT

JUL -8 2002
TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office Action mailed May 29, 2002, Applicant hereby elects claims 1-24 of Group I for prosecution.

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**PATENT** 

Hong Koo Kim

Application No.: 09/747,779

Page 2

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-326-2400.

Respectfully submitted,

Chun-Pok Leung Reg. No. 41,405

TOWNSEND and TOWNSEND and CREW LLP Two Embarcadero Center, 8<sup>th</sup> Floor San Francisco, California 94111-3834 Tel: (415) 576-0200

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TRANSMITTAL FORM  (See used for all correspondence after initial filing)			Application Number		09/747,779			
			Filing	Date	December 22,	December 22, 2000		
			First i	Named Inventor	Kim, Hong Koo			
			Group	Art Unit	2814			
			Exami	ner Name	Marcos D. Pizarro Crespo			
Total Number of Pages in This Submission			Attorn	ey Docket Number	000939-073311US			
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Response to Missing Parts under 37 CFR 1.52 or 1.53						JUL -8 2002	ी	
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and Individual name	Chun-Pok Leung			Reg. No	8			
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